

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	49	("6697979" "6003148" "6061808" "6061808" "6418067" "6395957" "6409881" "6436234" "5668774" "5844849" "6461476" "6429253" "5895751" "4609714" "4613645" "4804723" "4891167" "4965310" "5037936" "5982679" "6081903" "6968452" "4536704" "6246617" "6185709" "6324106" "6462995" "4767838" "4908425" "4959423" "5326834" "5748679" "5892798" "6117943" "6696528" "6835969" "6949978" "6426072" "5335199" "6496429" "6864869" "4965511" "5592493" "5386392" "5442641" "5557619" "6081910" "5289377" "5812466" "6037799").pn.	USPAT	OR	ON	2007/01/26 11:52
S2	2	"20040181726"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/26 11:53
S3	8	"2067477".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/26 12:18
S4	4	"3242998".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/26 15:05
S5	406307	(data adj process\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/26 15:09
S6	192964	(data adj process\$4) and (circuit IC chip)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/26 15:26
S7	8332	(data adj process\$4) and (circuit IC chip) and (first near2 program)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/26 15:54

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S8	2256	(data adj process\$4) and (circuit IC chip) and (first near2 program) and second near program	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/26 15:55
S9	2256	(data adj process\$4) and (circuit IC chip) and (first near2 program) and (second near program)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/08/15 21:07
S10	18	(data adj process\$4) and (circuit IC chip) and ((first near2 program) same (wiat\$3 idle)) and (second near program)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/26 16:14
S11	0	S1 and S10	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/26 16:14
S12	571	(data adj process\$4) and (circuit IC chip) and (((first and second) near2 program)) and (wiat\$3 idle))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/26 16:19
S13	290	(data adj process\$4) and (circuit IC chip) and (((first and second) near2 program)) and (wiat\$3 idle)) and (cell FPGA PLA (programmable adj logic))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/26 16:20
S14	1041	(data adj process\$4) and (circuit IC chip) and (first near2 program) and (second near program) and (wait idle)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/08/15 21:35
S15	1701	(data adj process\$4) and (circuit IC chip) and (first near2 program) and (second near2 program) and (wait idle)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/08/15 21:36
S16	281	(data adj process\$4) and (circuit IC chip) and (first near2 program) and ((second near2 program) with (after subsequently subsequent following succeeding ensuing successive successivly later)) and (wait idle)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/08/15 21:40

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S17	160	(data adj process\$4) and (circuit IC chip) and (first near2 program) and ((second near2 program) same (after subsequently subsequent following succeeding ensuing successive successivly later) same (reload storing store saved)) and (wait idle)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/08/15 21:42
S18	69	(data adj process\$4) and (circuit IC chip) and (first near2 program) and ((second near2 program) same (after subsequently subsequent following succeeding ensuing successive successivly later) same ((reload storing store stored loading saved) near3 data)) and (wait idle)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/08/15 21:43